IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Andrew E. Horch

Serial No.: 10/639,058 Filed: August 11, 2003

Title: Thyristor Semiconductor Memory

Device and Method of Manufacture

Group Art Unit: 2814

Examiner: Vikki H. Trinh

Confirmation No. 6760

§ Atty. Docket: 2000.004.00/US

Date: August 15, 2005

SUPPLEMENTAL AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 CERTIFICATE OF FACSIMILE SUBMISSION 37 CFR 1.8

I hereby certify that this correspondence is being facsimile transmitted to the United States Postal Service to Fax No. 571-273-1719, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on

Walter D. Piold

Dear Sir:

Applicant submits this amendment responsive to the Examiner's informality inquiry of earlier today and supplemental to applicant's previous amendment of June 20, 2005, which was submitted responsive to the Office Action dated March 30, 2005.

Amendment to the Specification begins on page 2.

Remarks/Arguments begin on page 3.

FIELDS IP, PS

Intellectual Property Services

Walter Fields 601 Main Street, Suite 405 Vancouver, Washington 98660 Telephone: (360) 750-9936 Fax: (360) 838-0144 walt@fieldsip.com

FACSIMILE COVER SHEET

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August 15, 2005

TO:

Examiner - Vikki H. Trinh

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Group:

2814

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Filed:

Aug 11, 2003 Andrew Horch

Inv.: Atty Dkt: 2000.004.00/US

Number of Pages (including this cover sheet)

Comments:

Responsive to an inquiry of this morning, please find a facsimile submission for a supplemental reply.

Feel free to contact us with any questions.

Walter D. Fields